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<u>L23</u>	17 and 112	23	<u>L23</u>
<u>L22</u>	17 and 114	386	<u>L22</u>
<u>L21</u>	17 and 115	471	<u>L21</u>
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<u>L16</u>	18 and 111	129	<u>L16</u>
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<u>L15</u>	(712/230-242)![CCLS]	2336	<u>L15</u>
<u>L14</u>	(712/235-242)![CCLS]	1679	<u>L14</u>
<u>L13</u>	(712/239-242)![CCLS]	1150	<u>L13</u>
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<u>L9</u>	L7 and thread\$3 and token\$1	32	<u>L9</u>
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<u>L5</u>	branch\$4 near10 (guess\$5 or speculat\$5 or predict\$6)	7319	<u>L5</u>
<u>L4</u>	(convert\$3 or conversion\$1 or translat\$4) near15 (microinstruction\$1 or instruction\$1 or micro near1 code\$1)	46972	<u>L4</u>
<u>L3</u>	(instruction\$1) nea15 (clock\$1 or cycle\$1 or period\$1) near15 (delay\$5 or wait\$5) near35 (number\$1 or second or first or count\$3) near45 branch\$5	895752	<u>L3</u>
<u>L2</u>	(instruction\$1) nea15 (clock\$1 or cycle\$1 or period\$1) near15 (delay\$5 or wait\$5) near35 (number\$1 or second or first or count\$3)	962480	<u>L2</u>
<u>L1</u>	(instruction\$1) nea15 (clock\$1 or cycle\$1 or period\$1) near15 (delay\$5 or wait\$5) near35 (number\$1 or second or first)	955054	<u>L1</u>

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Computer-Aided Design of Integrated Circuits and Systems, IEEE Transactions on

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